

## INVENTOR INFORMATION

Inventor One Given Name:: Janardhanan S  
Family Name:: AJIT  
Postal Address Line One:: 12 Woodrush  
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State or Province:: California  
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Citizenship Country:: United States

## CORRESPONDENCE INFORMATION

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## APPLICATION INFORMATION

Title Line One:: I/O Circuit Using Low Voltage Transistor  
Title Line Two:: s Which Can Tolerate High Voltages Even  
Title Line Three:: When Power Supplies are Powered Off  
Total Drawing Sheets:: 27  
Formal Drawings?: Yes  
Application Type:: Utility  
Docket Number:: 1875.2120003  
Secrecy Order in Parent Appl.?: No

## CONTINUITY INFORMATION

This application is a:: CONTINUATION IN PART OF  
> Application One:: 10/043,788  
Filing Date:: 01-09-2002

Which is a: NON PROV. OF PROVISIONAL  
>> Application Two:: 60/260,580  
Filing Date:: 01-09-2001

And which is a: NON PROV. OF PROVISIONAL  
>> Application Three:: 60/260,582  
Filing Date:: 01-09-2001

This application is a: NON PROV. OF PROVISIONAL  
> Application Four:: 60/427,954  
Filing Date:: 11-21-2002

Source:: PrintEFS Version 1.0.1